

## Features

- 16 async serial channels
- 50 to 38400 baud
- RS-232 and RS-422 data interface, selectable per line
- Bidirectional Flow Control: XON/XOFF, CTS, and DSR
- Modem Signal Support: RTS, CTS, DCD, DSR, DTR
- Event Detection: Ring, Break, and Disconnect
- Full duplex DMP data transfers through the IPC
- Stop Bits: 1, 1.5, and 2
- Parity: None, Odd, Even, Mark, Space
- Uses ISA 16-bit bus with shared memory interface
- NEC V53 (80286 compatible) on-board processor, 12.5 MHz
- 128 KB on-board memory, expandable to 2 MB
- Requires 1 IPC 16-bit slot
- Expandable to 64 channels
- Rack-mount DB-25 interface panel
- IPC Async Driver and Handler included
- Compatible with standard async communications, TMP, and TSX for MAX IV, revision F.0 and later, and MAX 32, B.0 and later

# IPC

## 3200 Multiline Serial Communications Interface

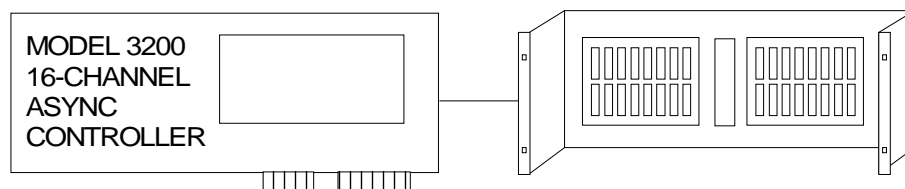


Figure 1: Multiline Serial Communications Interface

The Multiline Serial Communications Interface provides 16 channels of asynchronous communications supporting RS-232 and RS-422 line interfaces at data rates up to 38400 baud on a single PC card. A rack-mounted interface panel provides mounting for 16 or 32 DB-25 connections with a flexible cable to connect to the IPC chassis. The controller requires one 16-bit slot in the IPC. Up to four 16-channel interfaces can be supported in a single IPC, providing a maximum of 64 channels in a 7-inch high rack chassis.

Complete MODCOMP compatible handler processing is provided by the IPC Async Handler, supporting all ASCII and Binary modes while reducing MODCOMP CPU

requirements. Data is transferred using direct memory (DMP) transfers, eliminating interrupt overhead.

To insure maximum performance, each interface has its own processor (NEC V53) and 128 KB memory (expandable to 2.0 MB).

Both full and half duplex communications are supported. Break, Ring, and Disconnect events can be detected and queued as TCL events for TMP and TSX support of dial-up modems. Data flow control is supported in both directions using RS-232 signals and programmable control characters like XON/XOFF. Input data buffering permits type-ahead. Echo control is provided in the IPC based on LDT or read options.

